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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Yasushi UENO et al.

Date: May 27, 1998

Serial No.: 09/075,668

Group Art Unit:

Filed: May 11, 1998

Examiner:

For: LAMINATED CERAMIC ELECTRONIC PARTS

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Please reconsider the above-identified application  
amended as follows:

IN THE SPECIFICATION:

Please amend the specification as follows:

Page 1, delete lines 12-30; and insert

--A typical laminated ceramic electronic part in the form of a surface mounted laminated ceramic capacitor is shown in Fig. 2. Capacitor 23 is constructed by disposing a pair of external terminals 24a and 24b, which are in electrical contact with a plurality of internal electrodes 22, at opposite ends of a ceramic dielectric block 21. The dielectric block 21 is formed of a lamination of thin ceramic layers 21a having the internal electrodes 22 disposed therebetween. This laminated structure makes it possible to obtain a large capacitance even though the overall size of the capacitor 22 is small.